

IN THE CLAIMS:

1. (Currently amended) A low-capacitance laminate varistor comprising:
at least one pair of first and second inner electrodes each having a first end
and an opposing second end;
a varistor layer, and
a first outer electrode and a second outer electrode, electrically connected to
said first inner electrode first end and a said second inner electrode first end,
respectively;

wherein said first inner electrode and said second inner electrode are
dimensioned and configured on a same plane wherein ~~an electrode surface~~ said
opposing second end of said first inner electrode and ~~an electrode surface~~ said
opposing second end of said second inner electrode are in a non-facing relation to
one another and are laterally displaced from one another.

2. (Original) The low-capacitance laminate varistor according to claim 1,
wherein said first and second electrodes of said pair are separated by varistor layer
and formed on different planes.

3. (Original) The low-capacitance laminate varistor according to claim 1,
wherein said pair of first and second inner electrodes formed on one and the same
plane of said varistor layer are in multiples.

4. (Original) The low-capacitance laminate varistor according to claim 2, wherein said pair of first and second inner electrodes formed on one and the same plane of said varistor layer are in multiples.

5. (Original) The low-capacitance laminate varistor according to claim 1, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

6. (Original) The laminate varistor according to claim 2, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

7. (Original) The laminate varistor according to claim 5, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.

8. (Original) The laminate varistor according to claim 6, wherein the length of said pair of first and second inner electrodes is greater, equal to, or smaller than the width of varistor layer.